Cadence Tutorial D Using Design Variables And Parametric

Formal Methods in Computer-Aided Design
Third International Conference, FMCAD 2000 Austin, TX, USA, November 1-3, 2000 Proceedings
Selected Contributions from FDL 2015
Quantifying and Exploring the Gap Between FPGAs and ASICs
Distributed Design Data Management for Electronic Design Automation
Analysis and Design of Digital Systems with VHDL
Advances in Communication, Signal Processing, VLSI, and Embedded Systems
Smart Sensors for Industrial Applications
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FPGA ...
Proceedings of the ... European Symposium on Reliability of Electron Devices, Failure Physics and Analysis
Electronic Products Magazine
Modeling, Analyzing and Mitigating Signal
Electromigration in NanoCMOS
Proceedings
Electromigration Inside Logic Cells
First International Conference, FMCAD '96, Palo Alto, CA, USA, November 6 - 8, 1996, Proceedings
Complete PCB Design Using OrCAD Capture and PCB Editor
Languages, Design Methods, and Tools for Electronic System Design
Yield-Aware Analog IC Design and Optimization in Nanometer-scale Technologies
Analog, Mixed-Signal, and Mixed-Technology Modeling
Proceedings of the 2nd European Workshop held in Noordwijkerhout, The Netherlands, 14–15 May 1998
Complete PCB Design Using OrCad Capture and Layout
Integrated Circuit Test Engineering
The System Designer's Guide to VHDL-AMS

Adaptive Digital Circuits for Power-Performance Range beyond Wide Voltage Scaling
Electronic Engineering
Modern Techniques
Processor Design
System Level ESD Co-Design

KATELYN ANGELICA

Formal Methods in Computer-Aided Design Springer Science & Business Media
Analog CMOS integrated circuits are in widespread use for communications, entertainment, multimedia, biomedical, and many other applications that interface with the physical world. Although analog CMOS design is greatly complicated by the design choices of drain current, channel width, and channel length present for every MOS device in a circuit, these design choices afford significant opportunities for optimizing circuit performance. This book addresses tradeoffs and optimization of device and circuit performance for selections of the drain current, inversion coefficient, and channel length, where channel width is implicitly considered. The inversion coefficient is used as a technology independent measure of MOS inversion that

permits design freely in weak, moderate, and strong inversion. This book details the significant performance tradeoffs available in analog CMOS design and guides the designer towards optimum design by describing: An interpretation of MOS modeling for the analog designer, motivated by the EKV MOS model, using tabulated hand expressions and figures that give performance and tradeoffs for the design choices of drain current, inversion coefficient, and channel length; performance includes effective gate-source bias and drain-source saturation voltages, transconductance efficiency, transconductance distortion, normalized drain-source conductance, capacitances, gain and bandwidth measures, thermal and flicker noise, mismatch, and gate and drain leakage current Measured data that validates the inclusion of important small-geometry effects like velocity saturation, vertical-field mobility reduction, drain-induced barrier lowering, and inversion-level increases in gate-referred, flicker noise voltage In-depth treatment of moderate inversion, which offers low bias compliance voltages, high transconductance efficiency, and good immunity to velocity saturation effects for circuits designed in modern, low-voltage processes Fabricated

design examples that include operational transconductance amplifiers optimized for various tradeoffs in DC and AC performance, and micropower, low-noise preamplifiers optimized for minimum thermal and flicker noise A design spreadsheet, available at the book web site, that facilitates rapid, optimum design of MOS devices and circuits Tradeoffs and Optimization in Analog CMOS Design is the first book dedicated to this important topic. It will help practicing analog circuit designers and advanced students of electrical engineering build design intuition, rapidly optimize circuit performance during initial design, and minimize trial-and-error circuit simulations.

Third International Conference, FMCAD 2000 Austin, TX, USA, November 1-3, 2000 Proceedings

John Wiley & Sons

The biannual Formal Methods in Computer Aided Design conference (FMCAD 2000)is the third in a series of conferences under that title devoted to the use of discrete mathematical methods for the analysis of computer hardware and so- ware. The work reported in this book describes the use of modeling languages and their associated automated analysis tools to specify and verify computing systems. Functional veric ation has become one of the principal costs in a modern computer

design e ort. In addition,verica tion of circuit models, timing,power, etc., requires even more eo rt. FMCAD provides a venue for academic and - dustrial researchers and practitioners to share their ideas and experiences of using discrete mathematical modeling and veric ation. It is noted with interest by the conference chairmen how this area has grown from just a few people 15 years ago to a vibrant area of research, development, and deployment. It is clear that these methods are helping reduce the cost of designing computing systems. As an example of this potential cost reduction, we have invited David Russino of Advanced Micro Devices, Inc. to describe his veric ation of ?oating-point - gorithms being used in AMD microprocessors. The program includes 30 regular presentations selected from 63 submitted papers.

Selected Contributions from FDL 2015 CRC Press

ANALYSIS AND DESIGN OF DIGITAL SYSTEMS WITH VHDL integrates industry-standard hardware description language (VHDL) technology into the undergraduate digital logic course. Author Allen Dewey observes that the widespread use of VHDL in specifying digital system designs is driving change and innovation in industry, and defining a new skill set that engineering

students must master to design, model, communicate, and implement digital systems. VHDL provides a formal mechanism for describing digital systems in a format easily processed by computers, succinctly capturing the basic concepts of digital systems engineering and harnessing the power of design automation technology. This book first presents combinational and sequential systems and their design, along with logic families and integrated circuits. It then interlocks these subjects with discussions of structural and data flow modeling, synchronous behavior, and algorithmic modeling of digital systems in VHDL. This dual-track organization of conceptual and VHDL-related material makes the book easily adaptable to one- or two-semester courses and a variety of teaching approaches.

Quantifying and Exploring the Gap Between FPGAs and ASICs Springer Nature Unrivalled in its coverage and unique in its hands-on approach, this guide to the design and construction of scientific apparatus is essential reading for every scientist and student of engineering, and physical, chemical, and biological sciences. Covering the physical principles governing the operation of the mechanical, optical and electronic parts of an instrument, new sections on detectors, low-temperature

measurements, high-pressure apparatus, and updated engineering specifications, as well as 400 figures and tables, have been added to this edition. Data on the properties of materials and components used by manufacturers are included. Mechanical, optical, and electronic construction techniques carried out in the lab, as well as those let out to specialized shops, are also described. Step-by-step instruction supported by many detailed figures, is given for laboratory skills such as soldering electrical components, glassblowing, brazing, and polishing. Distributed Design Data Management for Electronic Design Automation Digital Electronics and Design with VHDL Presenting a comprehensive overview of the design automation algorithms, tools, and methodologies used to design integrated circuits, the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes. The second volume, EDA for IC Implementation, Circuit Design, and Process Technology, thoroughly examines real-time logic to GDSII (a file format used to transfer data of semiconductor physical layout), analog/mixed signal design, physical verification, and technology CAD (TCAD). Chapters contributed by leading experts authoritatively discuss design for manufacturability at

the nanoscale, power supply network design and analysis, design modeling, and much more. Save on the complete set.

Analysis and Design of Digital Systems with VHDL Pws Publishing Company The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook, Second Edition, Electronic Design Automation for IC System Design, Verification, and Testing thoroughly examines system-level design, microarchitectural design, logic verification, and testing. Chapters contributed by leading experts authoritatively discuss processor modeling and design tools, using performance metrics to select microprocessor cores for integrated circuit (IC) designs, design and verification languages, digital simulation, hardware acceleration and emulation, and much more. New to This Edition: Major updates appearing in the initial phases of the design flow, where the level of abstraction keeps rising to support more functionality with lower non-recurring engineering (NRE) costs Significant revisions reflected in the final phases of the design flow, where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting-edge applications and

approaches realized in the decade since publication of the previous edition—these are illustrated by new chapters on high-level synthesis, system-on-chip (SoC) block-based design, and back-annotating system-level models Offering improved depth and modernity, Electronic Design Automation for IC System Design, Verification, and Testing provides a valuable, state-of-the-art reference for electronic design automation (EDA) students, researchers, and professionals.

Advances in Communication, Signal Processing, VLSI, and Embedded Systems Springer Science & Business Media

A professional guide to the fundamentals of power integrity analysis with an emphasis on silicon level power integrity Power Integrity for Electrical and Computer Engineers embraces the most recent changes in the field, offers a comprehensive introduction to the discipline of power integrity, and provides an overview of the fundamental principles. Written by noted experts on the topic, the book goes beyond most other resources to focus on the detailed aspects of silicon and optimization techniques in order to broaden the field of study. This important book offers coverage of a wide range of topics including signal analysis, EM concepts for PI, frequency domain analysis for PI,

numerical methods (overview) for PI, and silicon device PI modeling. Power Integrity for Electrical and Computer Engineers examine platform technologies, system considerations, power conversion, system level modeling, and optimization methodologies. To reinforce the material presented, the authors include example problems. This important book: • Includes coverage on convergence, accuracy, and error analysis and explains how these can be used to analyze power integrity problems • Contains information for modeling the power converter from the PDN to the load in a full system level model • Explores areas of device level modeling of silicon as related to power integrity • Contains example word problems that are related to an individual chapter's subject Written for electrical and computer engineers and academics, Power Integrity for Electrical and Computer Engineers is an authoritative guide to the fundamentals of power integrity and explores the topics of power integrity analysis, power integrity analytics, silicon level power integrity, and optimization techniques.

Smart Sensors for Industrial Applications John Wiley & Sons

Using the book and the software provided with it, the reader can build his/her own tester arrangement to

investigate key aspects of analog-, digital- and mixed system circuits Plan of attack based on traditional testing, circuit design and circuit manufacture allows the reader to appreciate a testing regime from the point of view of all the participating interests Worked examples based on theoretical bookwork, practical experimentation and simulation exercises teach the reader how to test circuits thoroughly and effectively

Annual Report of the European Organization for Nuclear Research

Morgan Kaufmann Digital Electronics and Design with VHDL offers a friendly presentation of the fundamental principles and practices of modern digital design. Unlike any other book in this field, transistor-level implementations are also included, which allow the readers to gain a solid understanding of a circuit's real potential and limitations, and to develop a realistic perspective on the practical design of actual integrated circuits. Coverage includes the largest selection available of digital circuits in all categories (combinational, sequential, logical, or arithmetic); and detailed digital design techniques, with a thorough discussion on state-machine modeling for the analysis and design of complex sequential systems. Key technologies used in modern circuits are also described,

including Bipolar, MOS, ROM/RAM, and CPLD/FPGA chips, as well as codes and techniques used in data storage and transmission. Designs are illustrated by means of complete, realistic applications using VHDL, where the complete code, comments, and simulation results are included. This text is ideal for courses in Digital Design, Digital Logic, Digital Electronics, VLSI, and VHDL; and industry practitioners in digital electronics. Comprehensive coverage of fundamental digital concepts and principles, as well as complete, realistic, industry-standard designs Many circuits shown with internal details at the transistor-level, as in real integrated circuits Actual technologies used in state-of-the-art digital circuits presented in conjunction with fundamental concepts and principles Six chapters dedicated to VHDL-based techniques, with all VHDL-based designs synthesized onto CPLD/FPGA chips

Select Proceedings of VSPICE 2019 Springer The power consumption of integrated circuits is one of the most problematic considerations affecting the design of high-performance chips and portable devices. The study of power-saving design methodologies now must also include subjects such as systems on chips,

embedded software, and the future of microelectronics. Low-Power Electronics Design covers all major aspects of low-power design of ICs in deep submicron technologies and addresses emerging topics related to future design. This volume explores, in individual chapters written by expert authors, the many low-power techniques born during the past decade. It also discusses the many different domains and disciplines that impact power consumption, including processors, complex circuits, software, CAD tools, and energy sources and management. The authors delve into what many specialists predict about the future by presenting techniques that are promising but are not yet reality. They investigate nanotechnologies, optical circuits, ad hoc networks, e-textiles, as well as human powered sources of energy. Low-Power Electronics Design delivers a complete picture of today's methods for reducing power, and also illustrates the advances in chip design that may be commonplace 10 or 15 years from now.

Proceedings of ASP-DAC/VLSI Design 2002

Springer

An effective and cost efficient protection of electronic system against ESD stress pulses specified by IEC 61000-4-2 is paramount for any system design. This pioneering book presents the collective

knowledge of system designers and system testing experts and state-of-the-art techniques for achieving efficient system-level ESD protection, with minimum impact on the system performance. All categories of system failures ranging from 'hard' to 'soft' types are considered to review simulation and tool applications that can be used. The principal focus of System Level ESD Co-Design is defining and establishing the importance of co-design efforts from both IC supplier and system builder perspectives. ESD designers often face challenges in meeting customers' system-level ESD requirements and, therefore, a clear understanding of the techniques presented here will facilitate effective simulation approaches leading to better solutions without compromising system performance. With contributions from Robert Ashton, Jeffrey Dunnihoo, Micheal Hopkins, Pratik Maheshwari, David Pomerenke, Wolfgang Reinprecht, and Matti Usumaki, readers benefit from hands-on experience and in-depth knowledge in topics ranging from ESD design and the physics of system ESD phenomena to tools and techniques to address soft failures and strategies to design ESD-robust systems that include mobile and automotive applications. The first dedicated resource to system-level ESD co-

design, this is an essential reference for industry ESD designers, system builders, IC suppliers and customers and also Original Equipment Manufacturers (OEMs). Key features: Clarifies the concept of system level ESD protection. Introduces a co-design approach for ESD robust systems. Details soft and hard ESD fail mechanisms. Detailed protection strategies for both mobile and automotive applications. Explains simulation tools and methodology for system level ESD co-design and overviews available test methods and standards. Highlights economic benefits of system ESD co-design.

ACM/SIGDA International Symposium on Field Programmable Gate Arrays Cambridge University Press

This book comprises selected peer-reviewed papers from the International Conference on VLSI, Signal Processing, Power Systems, Illumination and Lighting Control, Communication and Embedded Systems (VSPICE-2019). The contents are divided into five broad topics - VLSI and embedded systems, signal processing, power systems, illumination and control, and communication and networking. The book focuses on the latest innovations, trends, and challenges encountered in the different areas of electronics and communication, and

electrical engineering. It also offers potential solutions and provides an insight into various emerging areas such as image fusion, bio-sensors, and underwater sensor networks. This book can prove to be useful for academics and professionals interested in the various sub-fields of electronics and communication engineering.

AutoCAD and Its Applications Springer

Digital Electronics and Design with VHDLMorgan Kaufmann

EDA for IC Implementation, Circuit Design, and Process Technology CRC Press

Anyone involved in circuit design that needs the practical know-how it takes to design a successful circuit or product, will find this practical guide to using Capture-PSpice (written by a former Cadence PSpice expert for Europe) an essential book. The text delivers step-by-step guidance on using Capture-PSpice to help professionals produce reliable, effective designs. Readers will learn how to get up and running quickly and efficiently with industry standard software and in sufficient detail to enable building upon personal experience to avoid common errors and pit-falls. This book is of great benefit to professional electronics design engineers, advanced amateur electronics designers, electronic engineering students and academic staff looking for a book

with a real-world design outlook. Provides both a comprehensive user guide, and a detailed overview of simulation Each chapter has worked and ready to try sample designs and provides a wide range of to-do exercises Core skills are developed using a running case study circuit Covers Capture and PSpice together for the first time

From the Clock Path to the Data Path Springer Nature

This book constitutes the refereed proceedings of the First International Conference on Formal Methods in Computer-Aided Design, FMCAD '96, held in Palo Alto, California, USA, in November 1996. The 25 revised full papers presented were selected from a total of 65 submissions; also included are three invited survey papers and four tutorial contributions. The volume covers all relevant formal aspects of work in computer-aided systems design, including verification, synthesis, and testing.

Proceedings, ... International Symposium on VLSI Design CRC Press

A comprehensive, easy-to-understand guide that shows how to apply AutoCAD functions to typical drafting and graphic design tasks. This is not just another version of the reference manual, but a text designed to help you master AutoCAD.

DQ. Springer Science & Business Media

Papers from a January 2002 conference are

organized into four sessions each on low power design, synthesis, testing, layout, and interconnects and technology, as well as two sessions each on embedded systems, verification, and VLSI architecture, one session on analog design, and one session on hot c

Digital Electronics and Design with VHDL

Springer Science & Business Media

Field-programmable gate arrays (FPGAs), which are pre-fabricated, programmable digital integrated circuits (ICs), provide easy access to state-of-the-art integrated circuit process technology, and in doing so, democratize this technology of our time. This book is about comparing the qualities of FPGA – their speed performance, area and power consumption, against custom-fabricated ICs, and exploring ways of mitigating their de ciencies. This work began as a question that many have asked, and few had the resources to answer – how much worse is an FPGA compared to a custom-designed chip? As we dealt with that question, we found that it was far more dif cult to answer than we anticipated, but that the results were rich basic insights on fundamental understandings of FPGA architecture. It also encouraged us to nd ways to leverage those insights to seek ways to make FPGA technology better, which is what the

second half of the book is about. While the question “How much worse is an FPGA than an ASIC?” has been a constant sub-theme of all research on FPGAs, it was posed most directly, some time around May 2004, by Professor Abbas El Gamal from Stanford University to us – he was working on a 3D FPGA, and was wondering if any real measurements had been made in this kind of comparison. Shortly thereafter we took it up and tried to answer in a serious way.

Analog Design and Simulation Using OrCAD Capture and PSpice Springer Science & Business Media

Complete PCB Design Using OrCad Capture and Layout provides instruction on how to use the OrCAD design suite to design and manufacture printed circuit boards. The book is written for both students and practicing engineers who need a quick tutorial on how to use the software and who need in-depth knowledge of the capabilities and limitations of the software package. There are two goals the book aims to reach: The primary goal is to show the reader how to design a PCB using OrCAD Capture and OrCAD Layout. Capture is used to build the schematic diagram of the circuit, and Layout is used to design the circuit board so that it can be manufactured. The secondary goal is to show the reader how to add PSpice simulation capabilities to the

design, and how to develop custom schematic parts, footprints and PSpice models. Often times separate designs are produced for documentation, simulation and board fabrication. This book shows how to perform all three functions from the same schematic design. This approach saves time and money and ensures continuity between the design and the manufactured product. Information is presented in the exact order a circuit and PCB are designed Straightforward, realistic examples present the how and why the designs work, providing a comprehensive toolset for understanding the OrCAD software Introduction to the IPC, JEDEC, and IEEE standards relating to PCB design Full-color interior and extensive illustrations allow readers to learn features of the product in the most realistic manner possible

FPGA ... Elsevier

This book presents a new methodology with reduced time impact to address the problem of analog integrated circuit (IC) yield estimation by means of Monte Carlo (MC) analysis, inside an optimization loop of a population-based algorithm. The low time impact on the overall optimization processes enables IC designers to perform yield optimization with the most accurate yield estimation method, MC simulations using foundry statistical device models considering local and

global variations. The methodology described by the authors delivers on average a reduction of 89% in the total number of MC simulations, when compared to the exhaustive MC analysis over the full population. In addition to describing a newly developed yield estimation technique, the authors also provide detailed background on automatic analog IC sizing and optimization.