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Hybrid ADCs, Smart Sensors for the IoT, and Sub-1V & Advanced Node Analog Circuit Design

Spacer Engineered FinFET Architectures

Sparse representation of visual data for compression and compressed sensing

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From VLSI Architectures to CMOS Fabrication

Digital Integrated Circuit Design

Digital Integrated Circuit Design South Asian Edition

System-on-Chip Security

34th International Conference, PETRI NETS 2013, Milan, Italy, June 24-28, 2013, Proceedings

Intelligent Computing and Applications

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Trade-Offs in Analog Circuit Design

Low-Power Cmos Vlsi Circuit Design

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Morgan Kaufmann
Very Large Scale Integration (VLSI) has become a necessity rather than a specialization for electrical and computer engineers. This unique text provides Engineering and Computer Science students with a comprehensive study of the subject, covering VLSI from basic design techniques to working principles of physical design automation tools to leading edge application-specific array processors. Beginning with CMOS design, the author describes VLSI design from the viewpoint of a digital circuit engineer. He develops physical pictures for CMOS circuits and demonstrates the top-down design methodology using two design projects - a microprocessor and a field programmable gate array. The author then discusses VLSI testing and dedicates an entire chapter to the working principles, strengths, and weaknesses of ubiquitous physical design tools. Finally, he unveils the

frontiers of VLSI. He emphasizes its use as a tool to develop innovative algorithms and architecture to solve previously intractable problems. VLSI Design answers not only the question of "what is VLSI," but also shows how to use VLSI. It provides graduate and upper level undergraduate students with a complete and congregated view of VLSI engineering.

Hybrid ADCs, Smart Sensors for the IoT, and Sub-1V & Advanced Node Analog Circuit Design

Springer Science & Business Media
This book focusses on the spacer engineering aspects of novel MOS-based device-circuit co-design in sub-20nm technology node, its process complexity, variability, and reliability issues. It comprehensively explores the FinFET/tri-gate architectures with their circuit/SRAM suitability and tolerance to random statistical variations.

Spacer Engineered FinFET Architectures Springer Science & Business Media
This book provides the first comprehensive, up-to-date and self-contained introduction to the

emergent field of Programmable Integrated Photonics (PIP). It covers both theoretical and practical aspects, ranging from basic technologies and the building of photonic component blocks, to design alternatives and principles of complex programmable photonic circuits, their limiting factors, techniques for characterization and performance monitoring/control, and their salient applications both in the classical as well as in the quantum information fields. The book concentrates and focuses mainly on the distinctive features of programmable photonics, as compared to more traditional ASPIC approaches. After some years during which the Application Specific Photonic Integrated Circuit (ASPIC) paradigm completely dominated the field of integrated optics, there has been an increasing interest in PIP. The rising interest in PIP is justified by the surge in a number of emerging applications that call for true flexibility and reconfigurability, as well as low-cost, compact, and low-power consuming

devices. Programmable Integrated Photonics is a new paradigm that aims at designing common integrated optical hardware configurations, which by suitable programming, can implement a variety of functionalities. These in turn can be exploited as basic operations in many application fields. Programmability enables, by means of external control signals, both chip reconfiguration for multifunction operation, as well as chip stabilization against non-ideal operations due to fluctuations in environmental conditions and fabrication errors. Programming also allows for the activation of parts of the chip, which are not essential for the implementation of a given functionality, but can be of help in reducing noise levels through the diversion of undesired reflections.

Sparse representation of visual data for compression and compressed sensing

Springer Science & Business Media
This book is based on the 18 tutorials presented during the 26th workshop on Advances in Analog Circuit Design. Expert designers present readers

with information about a variety of topics at the frontier of analog circuit design, with specific contributions focusing on hybrid ADCs, smart sensors for the IoT, sub-1V and advanced-node analog circuit design. This book serves as a valuable reference to the state-of-the-art, for anyone involved in analog circuit research and development.

VLSI Analog Filters John Wiley & Sons

As the frequency of communication systems increases and the dimensions of transistors are reduced, more and more stringent performance requirements are placed on analog circuits. This is a trend that is bound to continue for the foreseeable future and while it does, understanding performance trade-offs will constitute a vital part of the analog design process. It is the insight and intuition obtained from a fundamental understanding of performance conflicts and trade-offs, that ultimately provides the designer with the basic tools necessary for effective and creative analog design. Trade-offs in Analog Circuit Design, which is devoted to the

understanding of trade-offs in analog design, is quite unique in that it draws together fundamental material from, and identifies interrelationships within, a number of key analog circuits. The book covers ten subject areas: Design methodology, Technology, General Performance, Filters, Switched Circuits, Oscillators, Data Converters, Transceivers, Neural Processing, and Analog CAD. Within these subject areas it deals with a wide diversity of trade-offs ranging from frequency-dynamic range and power, gain-bandwidth, speed-dynamic range and phase noise, to tradeoffs in design for manufacture and IC layout. The book has by far transcended its original scope and has become both a designer's companion as well as a graduate textbook. An important feature of this book is that it promotes an intuitive approach to understanding analog circuits by explaining fundamental relationships and, in many cases, providing practical illustrative examples to demonstrate the inherent basic interrelationships and trade-offs. Trade-offs in Analog Circuit Design draws together 34

contributions from some of the world's most eminent analog circuits-and-systems designers to provide, for the first time, a comprehensive text devoted to a very important and timely approach to analog circuit design.

Static Timing Analysis for Nanometer Designs

Prentice Hall

The summer school on VLSI GAD Tools and Applications was held from July 21 through August 1, 1986 at Beatenberg in the beautiful Bernese Oberland in Switzerland. The meeting was given under the auspices of IFIP WG 10.6 VLSI, and it was sponsored by the Swiss Federal Institute of Technology Zurich, Switzerland. Eighty-one professionals were invited to participate in the summer school, including 18 lecturers. The 81 participants came from the following countries: Australia (1), Denmark (1), Federal Republic of Germany (12), France (3), Italy (4), Norway (1), South Korea (1), Sweden (5), United Kingdom (1), United States of America (13), and Switzerland (39). Our goal in the planning for the summer school was to introduce the audience into the

realities of CAD tools and their applications to VLSI design. This book contains articles by all 18 invited speakers that lectured at the summer school. The reader should realize that it was not intended to publish a textbook.

However, the chapters in this book are more or less self-contained treatments of the particular subjects. Chapters 1 and 2 give a broad introduction to VLSI Design. Simulation tools and their algorithmic foundations are treated in Chapters 3 to 5 and 17. Chapters 6 to 9 provide an excellent treatment of modern layout tools. The use of CAD tools and trends in the design of 32-bit microprocessors are the topics of Chapters 10 through 16. Important aspects in VLSI testing and testing strategies are given in Chapters 18 and 19.

Nanowires John Wiley & Sons

Eine Idee für eine elektronische Schaltung - wie erhält man ein fertiges Gerät? Dies - und welche Schritte dazu notwendig sind - beantwortet dieses Buch. Es soll dem Elektronik-Designer systemunabhängig die wichtigsten Grundlagen zum Design von elektronischen

Schaltungen, Leiterplatten und Baugruppen vermitteln. Neben Betrachtungen zur Kondensation einer Idee zu einer konkreten Schaltung geht es um Planung, Darstellung der Funktion im Schema, Simulation, Design und Layout von Leiterplatten, sowie die Fertigung von Baugruppen.

The Designer's

Companion Springer

Many different kinds of FPGAs exist, with different programming technologies, different architectures and different software. Field-Programmable Gate Array Technology describes the major FPGA architectures available today, covering the three programming technologies that are in use and the major architectures built on those programming technologies. The reader is introduced to concepts relevant to the entire field of FPGAs using popular devices as examples. Field-Programmable Gate Array Technology includes discussions of FPGA integrated circuit manufacturing, circuit design and logic design. It describes the way logic and interconnect are implemented in various kinds of FPGAs. It covers particular problems with

design for FPGAs and future possibilities for new architectures and software. This book compares CAD for FPGAs with CAD for traditional gate arrays. It describes algorithms for placement, routing and optimization of FPGAs. Field-Programmable Gate Array Technology describes all aspects of FPGA design and development. For this reason, it covers a significant amount of material. Each section is clearly explained to readers who are assumed to have general technical expertise in digital design and design tools. Potential developers of FPGAs will benefit primarily from the FPGA architecture and software discussion. Electronics systems designers and ASIC users will find a background to different types of FPGAs and applications of their use.

Field-Programmable Gate Array Technology

Springer

Market_Desc: · Students in graduate level courses· Electrical Engineers· Computer Scientists· Computer Architecture Designers· Circuit Designers· Algorithm Designers· System Designers· Computer Programmers in the Multimedia and Wireless

Communications Industries· VLSI System Designers Special Features: This example-packed resource provides invaluable professional training for a rapidly-expanding industry. · Presents a variety of approaches to analysis, estimation, and reduction of power consumption in order to help designers extend battery life. · Includes application-driven problems at the end of each chapter· Features six appendices covering shortest path algorithms used in retiming, scheduling, and allocation techniques, as well as determining the iteration bound· The Author is a recognized expert in the field, having written several books, taught several graduate-level classes, and served on several IEEE boards About The Book: This book complements the other Digital Signaling Processing books in our list, which include an introductory treatment (Marven), a comprehensive handbook (Mitra), a professional reference (Kaloupsidis), and others which pertain to a specific topic such as noise control. This graduate level textbook will fill an important niche in a rapidly expanding

market.

VLSI Architecture KIT Scientific Publishing

This book pioneers the field of gain-cell embedded DRAM (GC-eDRAM) design for low-power VLSI systems-on-chip (SoCs). Novel GC-eDRAMs are specifically designed and optimized for a range of low-power VLSI SoCs, ranging from ultra-low power to power-aware high-performance applications. After a detailed review of prior-art GC-eDRAMs, an analytical retention time distribution model is introduced and validated by silicon measurements, which is key for low-power GC-eDRAM design. The book then investigates supply voltage scaling and near-threshold voltage (NTV) operation of a conventional gain cell (GC), before presenting novel GC circuit and assist techniques for NTV operation, including a 3-transistor full transmission-gate write port, reverse body biasing (RBB), and a replica technique for optimum refresh timing. Next, conventional GC bitcells are evaluated under aggressive technology and voltage scaling (down to the subthreshold domain), before novel bitcells for aggressively

scaled CMOS nodes and soft-error tolerance as presented, including a 4-transistor GC with partial internal feedback and a 4-transistor GC with built-in redundancy.

Cryptographic Hardware and Embedded Systems --

CHES 2010 Springer Science & Business Media

This is the first book devoted to low power circuit design, and its authors have been among the first to publish papers in this area. · Low-Power CMOS VLSI Design · Physics of Power Dissipation in CMOS FET Devices · Power Estimation · Synthesis for Low Power · Design and Test of Low-Voltage CMOS Circuits · Low-Power Static Ram Architectures · Low-Energy Computing Using Energy Recovery Techniques · Software Design for Low Power Microelectronics Cambridge University Press

Digital Systems Design with FPGAs and CPLDs explains how to design and develop digital electronic systems using programmable logic devices (PLDs). Totally practical in nature, the book features numerous (quantify when known) case study designs using a variety of Field

Programmable Gate Array (FPGA) and Complex Programmable Logic Devices (CPLD), for a range of applications from control and instrumentation to semiconductor automatic test equipment. Key features include: * Case studies that provide a walk through of the design process, highlighting the trade-offs involved. * Discussion of real world issues such as choice of device, pin-out, power supply, power supply decoupling, signal integrity- for embedding FPGAs within a PCB based design. With this book engineers will be able to: * Use PLD technology to develop digital and mixed signal electronic systems * Develop PLD based designs using both schematic capture and VHDL synthesis techniques * Interface a PLD to digital and mixed-signal systems * Undertake complete design exercises from design concept through to the build and test of PLD based electronic hardware This book will be ideal for electronic and computer engineering students taking a practical or Lab based course on digital systems development using PLDs and for engineers in industry

looking for concrete advice on developing a digital system using a FPGA or CPLD as its core. Case studies that provide a walk through of the design process, highlighting the trade-offs involved. Discussion of real world issues such as choice of device, pin-out, power supply, power supply decoupling, signal integrity- for embedding FPGAs within a PCB based design.

A Design Perspective

Prentice Hall

Beginning with

discussions on the

operation of electronic

devices and analysis of

the nucleus of digital

design, the text

addresses: the impact of

interconnect, design for

low power, issues in

timing and clocking,

design methodologies,

and the effect of design

automation on the digital

design perspective.

Low Voltage, Low Power

Springer Science &

Business Media

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numerous individuals, as

well as with prestigious organizations and societies, LNCS has grown into the most comprehensive computer science research forum available. The scope of LNCS, including its subseries LNAI and LNBI, spans the whole range of computer science and information technology including interdisciplinary topics in a variety of application fields. The type of material published traditionally includes proceedings (published in time for the respective conference) post-proceedings (consisting of thoroughly revised final full papers) research monographs (which may be based on outstanding PhD work, research projects, technical reports, etc.) More recently, several color-cover sublines have been added featuring, beyond a collection of papers, various added-value components; these sublines include tutorials (textbook-like monographs or collections of lectures given at advanced courses) state-of-the-art surveys (offering complete and mediated coverage of a topic) hot topics (introducing emergent topics to the broader community) In parallel to

the printed book, each new volume is published electronically in LNCS Online. Book jacket. High-Performance Digital Circuit Applications Top-Down Digital VLSI Design From Architectures to Gate-Level Circuits and FPGAs Gain an intuitive understanding of jitter and phase noise with this authoritative guide. Leading researchers provide expert insights on a wide range of topics, from general theory and the effects of jitter on circuits and systems, to key statistical properties and numerical techniques. Using the tools provided in this book, you will learn how and when jitter and phase noise occur, their relationship with one another, how they can degrade circuit performance, and how to mitigate their effects - all in the context of the most recent research in the field. Examine the impact of jitter in key application areas, including digital circuits and systems, data converters, wirelines, and wireless systems, and learn how to simulate it using the accompanying Matlab code. Supported by additional examples and exercises online, this is a one-stop guide for graduate students and

practicing engineers interested in improving the performance of modern electronic circuits and systems. *Digital Systems Design with FPGAs and CPLDs* Cambridge University Press
 Top-Down Digital VLSI Design From Architectures to Gate-Level Circuits and FPGAs Morgan Kaufmann
VLSI CAD Tools and Applications Springer Nature
 This book presents the peer-reviewed proceedings of the 5th International Conference on Intelligent Computing and Applications (ICICA 2019), held in Ghaziabad, India, on December 6–8, 2019. The contributions reflect the latest research on advanced computational methodologies such as neural networks, fuzzy systems, evolutionary algorithms, hybrid intelligent systems, uncertain reasoning techniques, and other machine learning methods and their applications to decision-making and problem-solving in mobile and wireless communication networks. *Validation and Verification* Cambridge University Press
 As the number of

processor cores and IP blocks integrated on a single chip is steadily growing, a systematic approach to design the communication infrastructure becomes necessary. Different variants of packed switched on-chip networks have been proposed by several groups during the past two years. This book summarizes the state of the art of these efforts and discusses the major issues from the physical integration to architecture to operating systems and application interfaces. It also provides a guideline and vision about the direction this field is moving to. Moreover, the book outlines the consequences of adopting design platforms based on packet switched network. The consequences may in fact be far reaching because many of the topics of distributed systems, distributed real-time systems, fault tolerant systems, parallel computer architecture, parallel programming as well as traditional system-on-chip issues will appear relevant but within the constraints of a single chip VLSI implementation. *Proceedings of ICICA 2019* Springer Science & Business Media

The ongoing advances in

computational photography have introduced a range of new imaging techniques for capturing multidimensional visual data such as light fields, BRDFs, BTFs, and more. A key challenge inherent to such imaging techniques is the large amount of high dimensional visual data that is produced, often requiring GBs, or even TBs, of storage. Moreover, the utilization of these datasets in real time applications poses many difficulties due to the large memory footprint. Furthermore, the acquisition of large-scale visual data is very challenging and expensive in most cases. This thesis makes several contributions with regards to acquisition, compression, and real time rendering of high dimensional visual data in computer graphics and imaging applications. Contributions of this thesis reside on the strong foundation of sparse representations. Numerous applications are presented that utilize sparse representations for compression and compressed sensing of visual data. Specifically, we present a single sensor light field camera design, a compressive

rendering method, a real time precomputed photorealistic rendering technique, light field (video) compression and real time rendering, compressive BRDF capture, and more. Another key contribution of this thesis is a general framework for compression and compressed sensing of visual data, regardless of the dimensionality. As a result, any type of discrete visual data with arbitrary dimensionality can be captured, compressed, and rendered in real time. This thesis makes two theoretical contributions. In particular, uniqueness conditions for recovering a sparse signal under an ensemble of multidimensional dictionaries is presented. The theoretical results discussed here are useful for designing efficient capturing devices for multidimensional visual data. Moreover, we derive the probability of successful recovery of a noisy sparse signal using OMP, one of the most widely used algorithms for solving compressed sensing problems. *Building Blocks for Nanoscience and Nanotechnology* Tata McGraw-Hill Education

This book provides a comprehensive summary of nanowire research in the past decade, from the nanowire synthesis, characterization, assembly, to the device applications. In particular, the developments of complex/modulated nanowire structures, the assembly of hierarchical nanowire arrays, and the applications in the fields of nanoelectronics,

nanophotonics, quantum devices, nano-enabled energy, and nano-bio interfaces, are focused. Moreover, novel nanowire building blocks for the future/emerging nanoscience and nanotechnology are also discussed. Semiconducting nanowires represent one of the most interesting research directions in nanoscience and

nanotechnology, with capabilities of realizing structural and functional complexity through rational design and synthesis. The exquisite control of chemical composition, morphology, structure, doping and assembly, as well as incorporation with other materials, offer a variety of nanoscale building blocks with unique properties.

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